

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Satoru TAKASE

Serial No.: Continuation of 10/348,965, filed
January 23, 2003

Filed: March 19, 2004

For: SEMICONDUCTOR DEVICE WITH
FLEXIBLE REDUNDANCY SYSTEM

Atty. Docket No.: 001701.00202

Group Art Unit: Unknown

Examiner: Unknown

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with Applicants' duty of disclosure, the following information is submitted for consideration by the United States Patent and Trademark Office in connection with the above-captioned application. The information is identified on the attached PTO-1449 form.

This application relies, under 35 U.S.C. § 120, on the earliest filing date of prior U.S. patent application serial nos. 10/348,965, filed January 23, 2003; 10/310,960, filed December 6, 2002; 09/953,307, filed September 17, 2001; 09/739,240, filed December 19, 2000; and 09/296,269, filed April 22, 1999. The documents identified on the attached PTO 1449 form were submitted to and/or cited by the Office in a prior application and, therefore, copies are not required to be provided in this application. (See 37 C.F.R. § 1.98(d)).

Applicants do not waive any right to take appropriate action to establish patentability over the listed documents should they be applied as references against the claims of the present application.

It is respectfully requested that the Examiner fully consider each of the documents, initial the enclosed Form PTO-1449 in the appropriate place to indicate that the document has been

considered, and return a copy of the initialed form to the undersigned in accordance with MPEP Section 609.

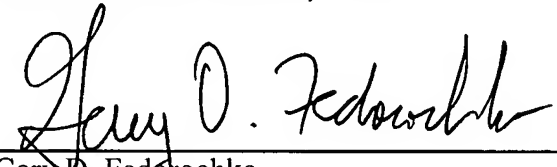
Applicants believe that no fee is necessary pursuant to 37 C.F.R. § 1.97(b). However, if a fee is due, the Office is authorized to charge Deposit Account No. 19-0733.

Respectfully submitted,

BANNER & WITCOFF, LTD.

Dated: March 29, 2004

By:



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GDF:lab

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PTO/SB/08A (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet

1

of

1

Complete if Known

Application Number	Continuation of 10/348,965
Filing Date	Herewith
First Named Inventor	Satoru TAKASE
Group Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	001701.00202

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
		5,430,679		Hiltebeitel et al.	07/1995	
		5,487,039		Sukegawa	01/23/1996	
		5,495,447		Butler et al.	02-27-1996	
		5,502,676		Pelley III, et al.	03/26/1996	
		5,684,746		Oowaki, et al.	11/1997	
		5,703,817		Shiratake, et al.	12/1997	
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		5,862,080		Harari et al.	01-19-1999	
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		6,542,420		Takase	4/2003	
		2003/0081471	A1	Takase	5-1-2003	

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ₆
		Office ³	Number ⁴	Kind Code ⁵ (if known)				
		JP	4-102295	A	Fujitsu Ltd.	04-03-1992		
		JP	2-53299	A	Suzuki	02/1990		

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Masahi Horiguchi et al., "A Flexible Redundancy Technique for High-Density DRAM's," Jan. 1991, IEEE Journal of Solid-State Circuits, Vol. 26, No. 1, pp. 12-17.	
		Toshiaki Kiriata et al., "Fault-Tolerant Designs for 256 Mb DRAM," April 1996, IEEE Journal of Solid-State Circuits, Vol. 31, No. 4, pp. 558-566	
		Betty Prince, "Semiconductor Memories," 1983, Wisley, 2 nd Ed., pp. 761-762	
		Betty Prince, "Semiconductor Memories," 1983, Wisley, 2 nd Ed., pp. 762-764	

Examiner
Signature

Date
Considered

¹EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.